Eighth International Workshop on
Constraints in Formal Verification (CFV), 2013
Program

8:50 – 9:00
OPENING REMARKS

9:00 – 10:30
SESSION 1
9:00 – 10:00
Invited Talk: Efficient Modular SAT Solving for Property-Directed Reachability
Thomas Ball (Microsoft)
10:00 – 10:30
Efficient Stochastic SAT Solving using Random Graphs
Seyed Ahmadyan, and Shobha Vasudevan (UIUC)

10:30 – 11:00
Coffee Break

11:00 – 12:30
SESSION 2
11:00 – 11:30
Minimization of QBF Skolem and Herbrand Certificates
Valeriy Balabanov, Shuo-Ren Lin, and Jie-Hong R. Jiang
(National Taiwan University)
11:30 – 12:00
Parameterized Abstractions for Reasoning about Algebraic Data Types
Tuan-Hung Pham, and Michael Whalen (University of Minnesota)
12:00 – 12:30
Towards Certifiable Loop Pipelining Transformations in Behavioral Synthesis
Disha Puri, Sandip Ray, and Fei Xie (Portland State University)

12:30 – 13:30
Lunch Break

13:30 – 15:00
SESSION 3
13:30 – 14:30
Invited Talk: LTL Satisfiability Checking
Kristin Rozier (NASA)
14:30 – 15:00
Debugging Missing Assumptions in a Formal Verification Environment
Brian Keng, Evian Qin, Andreas Veneris, and Djordje Maksimovic
(University of Toronto)

15:00 – 15:30
Coffee Break

15:30 – 16:30
SESSION 4
15:30 – 16:00
Finding Model-Checkable Needles in Large Source Code Haystacks: Modular
Bug-Finding via Static Analysis and Dynamic Invariant Discovery
Mohammad Alipour, Alex Groce, Chaoqiang Zhang, Anahita Sanadaj, and
Gokul Caushik (Oregon State University)
16:00 – 16:30
Assertion Generation and its Complete Checking with Small Numbers of
Test Vectors
Masahiro Fujita, Takeshi Matsumoto, and Satoshi Jo
(University of Tokyo)